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# WPA60R48DC Series

# 60 Watt Dual Output Quarter Brick DC/DC Converter



RoHS Compliant

• 2.3" x 1.45" x 0.40" (58.42mm x 36.83mm x 10.16mm)

- Dual Outputs Output 1/Output 2 Vdc: 1.5/1.0; 1.8/1.2; 2.2/1.5; 2.5/1.8; 3.3/1.2; 3.3/1.8; 3.3/2.5; 5.0/1.5; 5.0/3.3
- Independently Regulated Outputs
- 36V 75V Input Range

Remote On/Off Function

- Through-Hole Mounting
- Meets Basic Insulation Requirements of EN60950
- UL/CUL 60950, VDE EN60950 **Approved**
- Fixed Frequency Operation
- Minimum Load Requirements
- up Mode Output Over Current

setting Over Voltage Protection









The WPA60R48DC Series are high performance DC/DC converters provided on tack 60950. It is a two independent regulated outputs. For maximum flexibility, power can be between outputs as relielivers bie!

- Undervoltage Lock-Out
- Auto-Start
- Internal Temperature Shutdown
- Auto-Reset
- Soft Start

- Remote On/Off (Available in Positive or Negative Logic)
- Over Current Protection
- Overvoltage Protection
- Output Voltage Adjust on Both **Outputs**

mount components for increased reliability. WPA DC-DC converter modules are certified to UL/CUL 60950, and VDE to

delivers high performance across all line

The WPA60R48DC Series are assembled

by a fully automated process using surface

and load conditions.

PRODUCT SELECTION CHART 1						
	NOMINAL INPUT			OUTPUT CURRENT		TYPICAL
	VOLTAGE	OUTPUT	OUTPUT	MIN	MAX	EFFICIENCY
MODEL	(VDC)	#1	#2	LOAD(A)	LOAD (A)	(%)
WPA60R48D1510C*	48	1.5	1.0	0.0	12.0	82
WPA60R48D1812C*	48	1.8	1.2	0.0	12.0	82
WPA60R48D2215C*	48	2.2	1.5	0.0	12.0	86
WPA60R48D2518C*	48	2.5	1.8	0.0	12.0	86
WPA60R48D3312C*	48	3.3	1.2	0.0	12.0	89
WPA60R48D3318C*	48	3.3	1.8	0.0	12.0	89
WPA60R48D3325C*	48	3.3	2.5	0.0	12.0	89
WPA60R48D0515C*	48	5.0	1.5	0.0	12.0	89
WPA60R48D0533C*	48	5.0	3.3	0.0	12.0	90

Input current at nominal input line = 1.45A (Output Power = 60W)

<sup>\*</sup>Models available with -1 option (negative logic)

<sup>&</sup>lt;sup>1</sup>Total output power not to exceed 60 Watts

## **SPECIFICATIONS, ALL MODELS**

Specifications are at  $T_A = +25$ °C, Airflow = 300LFM (1.5m/s) at nominal input voltage unless otherwise specified.

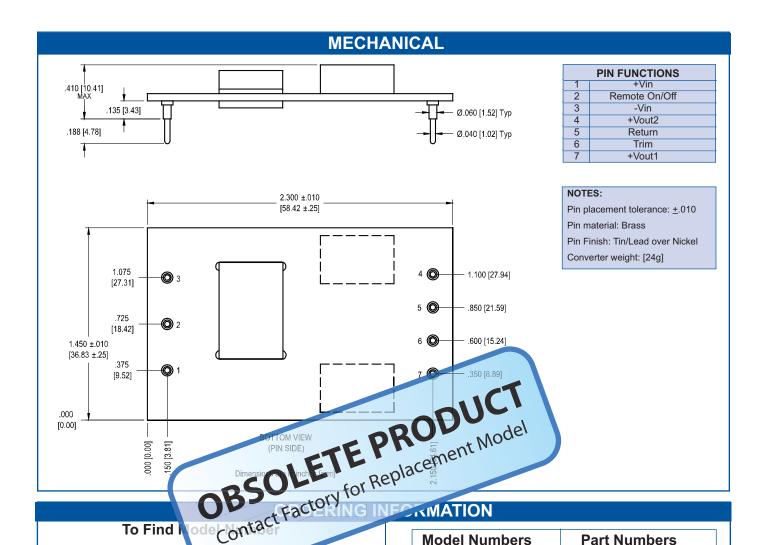
	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
	INPUT					
	Voltage Range	Vin = 48V, Io=I Rated	36	48	75	VDC
ы,	Reflected Ripple Current				675	mApk-pk
15						
<u> </u>	INPUT CONTROL					
Z	Temperature Shutdown	PCB		120		°C
	Temperature Hysteresis			5		°C
	Quiescent Standby Current	Vin = 48VDC		8	10	mA
	Undervoltage Lockout			32.5		V
	Undervoltage Shutdown			2		V

## **SPECIFICATIONS**

Specifications are at  $T_A = +25$ °C, Airflow = 300LFM (1.5m/s) at nominal input variage unless otherwise specified.

	PARAMETER	CONDITIONS	MIN	YP	MAX	UNITS
	ISOLATION					
	Rated Voltage		any	del		VDC
	Resistance	0	KO	Modific		$\Omega$ M
	Capacitance	-TEI	ment	1000		pF
	Leakage Current	200	Ulacer,	90		μArms
	ОИТРИТ	BSOLETE P	P			
	Rated Power	tactrac			60	W
	Voltage Setpoint Accuracy	ontae			±1.5	%
	Temperature Coefficient			±0.005		%/°C
	Output Voltage					
	(Over all conditions of I/P volta	V1			2	% of Nom
	load & temperature)	V2			8	% of Nom
7	Ripple & Noise (NOTE 1)	BW = 5Hz to 20MHz			90	mVp-p
\$	Output Adjust Range					
GENERAL	Trim Up				8	%
Z	Trim Down				5	%
끯	Short Circuit and					
Θ,	Overcurrent Protection			120		%
	Max Capacitive Load				10,000	μF
-	Overvoltage Protection			15		%
	GENERAL					
	Switching Frequency			350		KHz
	MTTF per ML-HDBK-217	Circuit Stress Method				
	Ground Benign	TA = +25° Unmodified Database		1,000,000		Hr
	Package Weight			30		g
	TEMPERATURE					
	Operation/Specification	PCB Temperature	-40		+100	°C
	Storage	PCBTemperature	-55		+125	°C
	Shutdown Temperature	PCB Temperature		+120		°C

NOTE 1: Measured at 20 MHz bandwidth across a 6µf multi layer ceramic capacitor located approximately 1" from output terminals.



To Find Node Ontoe				
WPA60R (8D yz	С			
Device Family				
60 Watt, Dual Output,				
Quarter Brick, 48VDC Input Range				
Model Number ———————				
Selected from Product Selection Chart (above)				
y = 15 = 1.5V, 18 = 1.8V, 22 = 2.2V, 25 = 2.5V,				
33 = 3.3V, 05 - 5.0V,				
z = 10 = 1.0V, 12 = 1.2V, 15 = 1.5V, 18 - 1.8V				
25 = 2.5V, 33 = 3.3V				
Remote On/Off Logic				
Positive Logic - No Number				
Negative Logic - 1				
RoHS Compliant ————————————————————————————————————				

Model Numbers	Part Numbers
WPA60R48D1510C	6064965
WPA60R48D1812C	6064957
WPA60R48D2215C	6064956
WPA60R48D2518C	6064955
WPA60R48D3312C	6064953
WPA60R48D3318C	6064952
WPA60R48D3325C	6064951
WPA60R48D0515C	6064966
WPA60R48D0533C	6064954
WPA60R48D1510-1C	6064993
WPA60R48D1812-1C	6064992
WPA60R48D2215-1C	6064991
WPA60R48D2518-1C	6064990
WPA60R48D3312-1C	6064989
WPA60R48D3318-1C	6064988
WPA60R48D3325-1C	6064987
WPA60R48D0515-1C	6064994
WPA60R48D0533-1C	6064986

#### THROUGH-HOLE SOLDERING INFORMATION

These devices are intended for wave soldering or manual soldering.

They are not intended to be subject to surface mount processes under any circumstances.

The normal wave soldering process can be used with these devices where the device is subjected to a maximum wave temperature of 260°C for a period of no more than 10 seconds. Within this time and temperature range, the integrity of the device's plastic body will not be compromised and internal temperatures within the converter will not exceed 175°C. Care should be taken to control manual soldering limits identical to that of wave soldering.

#### **APPLICATION NOTES**

# **Operation**

#### **Output Voltage Trim**

Each of the WPA60C's output voltages may be simultaneously adjusted above or below the nominal set point by a value as indicated on the Product Data Sheet. As shown in **Figure 1**, to raise the converter output voltage a resistor must be placed between the Trim pin and Return pin.

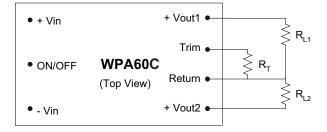


Figure 1 - Trim Up Circuit

To lower the converter output voltage a resistor must be placed between the Trim pin and Vout1 pin as shown in **Figure 2**.

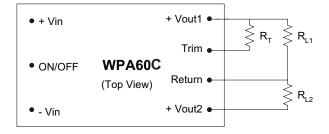


Figure 2 - Trim Down Circuit

The resistance value required to achieve the desired amount of positive/negative trim can be determined by referring to the trim graph for each model. If trimming is not desired then the Trim pin should be left unconnected.

#### **Remote ON/OFF Function**

The WPA60C is equipped with a primary ON/OFF pin used to remotely turn the converter on or off via a system signal. The input is TTL open-collector and/or FET open-drain compatible. For the positive logic model a system logic low signal will turn the unit off. For negative logic models a system logic high signal will turn the converter off. For negative logic models where no control signal will be used the ON/OFF pin should be connected directly to –Vin to ensure proper operation. For positive logic models where no control signal will be used the ON/OFF pin should be left open for normal operation.

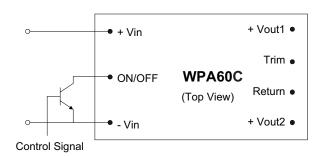


Figure 3 - Remote ON/OFF Control Circuit

#### **Protective Functions**

#### **Temperature Shutdown**

The over temperature shutdown feature of the WPA60C will cause the unit to shutdown at a typical pwb temperature of 120°C. This protective feature is comprised of a thermistor in the unit control loop. At a temperature of 120°C this circuit will cause the PWM to go into an idle mode, resulting in no output from the converter and preventing damage to the converter components. When the temperature of the unit drops below 120°C the fault condition will clear and the converter will resume normal operation. If the cause of the over temperature condition is not identified and corrected the unit will continue to cycle on and off.

#### **APPLICATION NOTES**

#### Input Under-Voltage Shutdown

The nominal input voltage for the WPA60C is 48Vdc. At an input voltage of 32.5Vdc nominal the unit will shutdown on an input under-voltage condition. At an input voltage less than 32.5V the under-voltage sensing circuit will send a signal to the PWM causing it to go into idle mode. This will result in no output from the converter, protecting the unit from a high input current condition. When the input voltage returns to a level above 32.5V the unit will return to normal operation. The unit will typically turn on at an input voltage of 34.5V nominal as indicated on the Product Data Sheet. This is due to hysterisis designed into the protective circuit to prevent excessive cycling of the converter.

#### **Over-Current Protection**

To protect against fault or short-circuit conditions on the output, each module is equipped with current-limiting circuitry designed to provide continuous protection. After reaching the current limit point (typically 20% above the rated output current), the voltage will range between its rated value and zero, depending upon the amount of overload. The unit will remain in operation continuously during this period down to a short-circuit condition. Once the short or overload has been eliminated, the output voltage will return to normal without cycling the input power.

#### Safety

The WPA60C meets safety requirements per UL/CUL 60950 and VDE to EN60950. Additionally, the converter meets CISPR22/EN55022/FCC15J Class B specs for EMI levels with external filtering.

#### **Performance Characterization**

#### **Thermal Derating**

Maximum ouput current vs. ambient temperature at various airflow rates has been determined for each model of the WPA60C. From these graphs, the combination of maximum ambient temperature and minimum airflow for select output current combinations can be determined. Each model was analyzed for maximum allowable output power over an ambient temperature range of 0 to 85°C and for airflows up to 600LFM. In each case the maximum allowable power at a given airflow and ambient temperature is defined as the point at which a known component reaches its individual temperature limit.

#### **Efficiency**

Efficiency data for each model was determined as a function of Load Current and Input Voltage. Efficiency vs. Load Current was measured at an ambient temperature of 25°C, an airflow of 300LFM with an input voltage of 48Vdc. Efficiency vs. Input Voltage was measured at an ambient temperature of 25°C, an airflow of 300LFM and rated load. Graphs for each model are provided in their respective section.

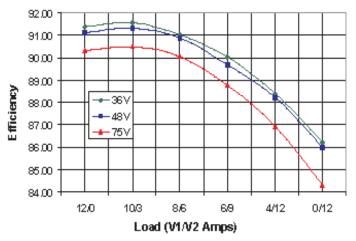
#### Start-Up, ON/OFF and Transient Response

For each model, waveforms are provided showing output voltage response and timing to input voltage power up/down, Remote ON/OFF state change and load current transient responses. Separate traces are provided showing the on/off timing sequence of the two outputs relative to one another. Output voltage transient responses are provided for step load changes of 50% - 100% of rated load current and 100% - 50% of rated load. Waveforms for each model are provided in their respective section.

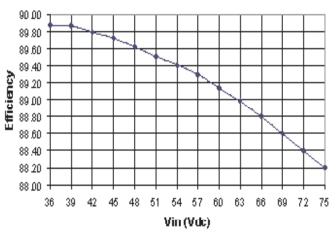
## PERFORMANCE CURVES: WPA60R48D0533C and WPA60R48D0533-1C

MODEL WPA60R48D0533C

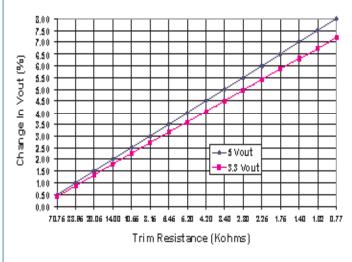
Efficiency vs. Output Current @ T<sub>A</sub> = +25°C



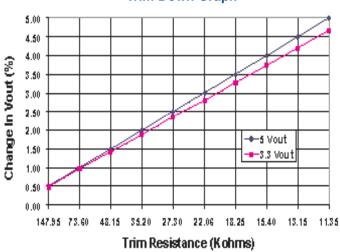
Efficiency vs. Input Voltage
@ T<sub>A</sub> = +25°C



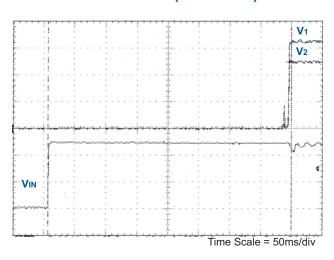
**Trim Up Graph** 



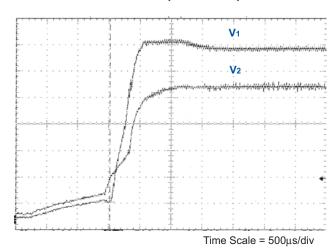
**Trim Down Graph** 



Turn On Time (VIN to VOUT)

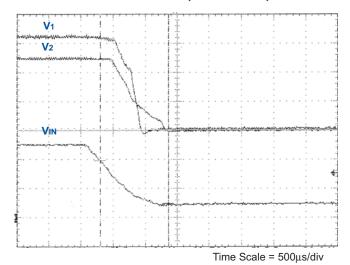


Rise Time (V<sub>1</sub> and V<sub>2</sub>)

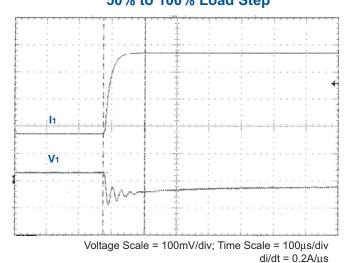


# PERFORMANCE CURVES: WPA60R48D0533C and WPA60R48D0533-1C

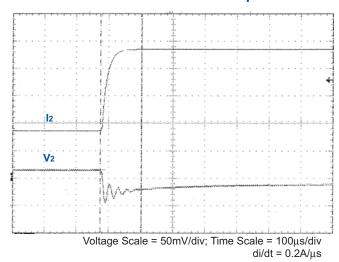
#### Turn Off Time (VIN to VOUT)



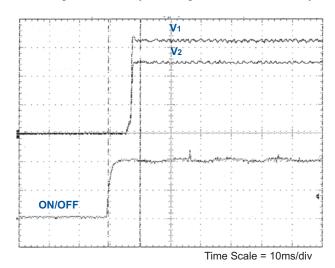
# CH1 (5.0Vdc) Transient Response 50% to 100% Load Step



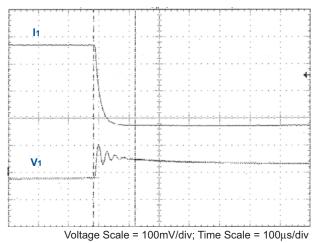
# CH2 (3.3Vdc) Transient Response 50% to 100% Load Step



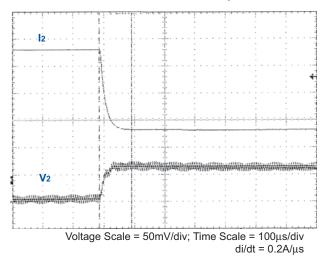
#### **Primary On Time (Primary Remote to Vout)**



### CH1 (5.0Vdc) Transient Response 100% to 50% Load Step

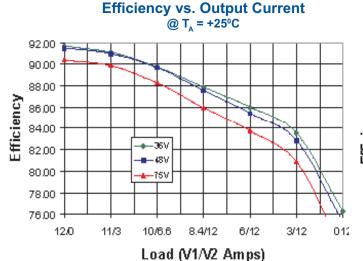


#### CH2 (3.3Vdc) Transient Response 100% to 50% Load Step

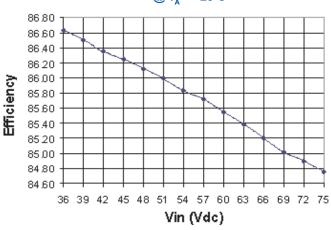


# PERFORMANCE CURVES: WPA60R48D0515C and WPA60R48D0515-1C

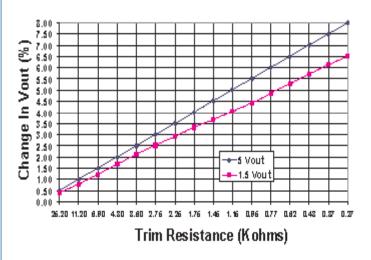
MODEL WPA60R48D0515C



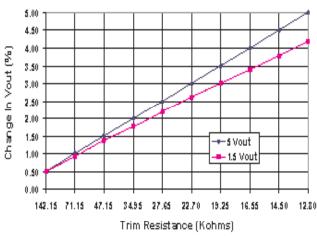
Efficiency vs. Input Voltage @  $T_A = +25^{\circ}C$ 



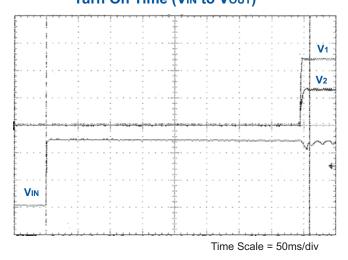
**Trim Up Graph** 



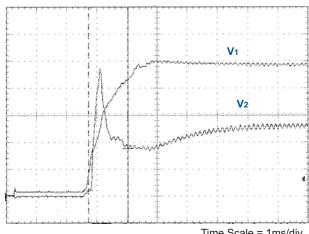
**Trim Down Graph** 



Turn On Time (VIN to Vouт)

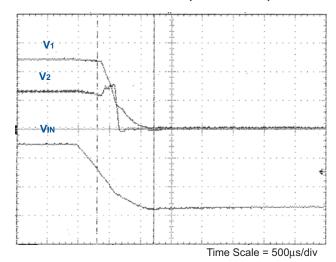


Rise Time (V<sub>1</sub> and V<sub>2</sub>)

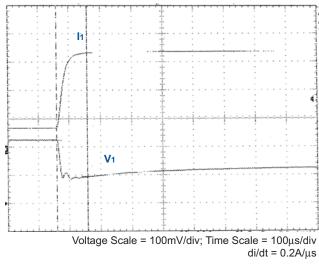


## PERFORMANCE CURVES: WPA60R48D0515C and WPA60R48D0515-1C

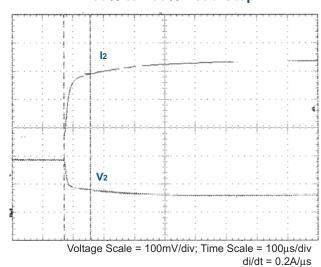
#### Turn Off Time (VIN to VOUT)



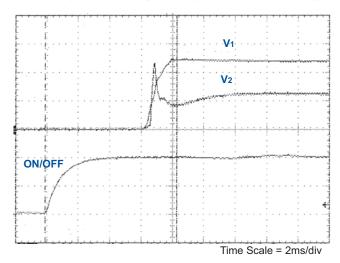
# CH1 (5.0Vdc) Transient Response 50% to 100% Load Step



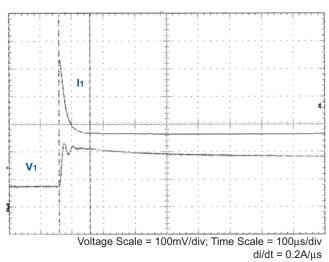
# CH2 (1.5Vdc) Transient Response 50% to 100% Load Step



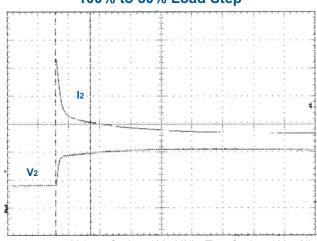
#### **Primary On Time (Primary Remote to Vout)**



## CH1 (5.0Vdc) Transient Response 100% to 50% Load Step



#### CH2 (1.5Vdc) Transient Response 100% to 50% Load Step

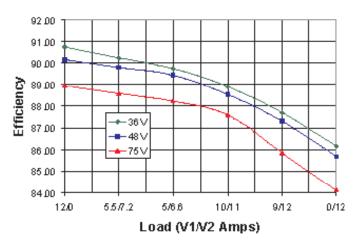


Voltage Scale = 100mV/div; Time Scale = 100µs/div di/dt = 0.2A/µs

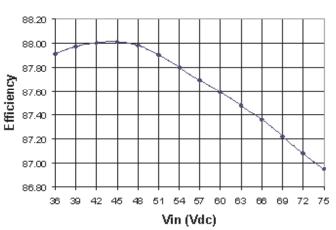
# PERFORMANCE CURVES: WPA60R48D3325C and WPA60R48D3325-1C

MODEL WPA60R48D3325C

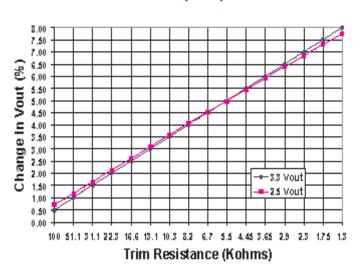
Efficiency vs. Output Current @ T<sub>a</sub> = +25°C



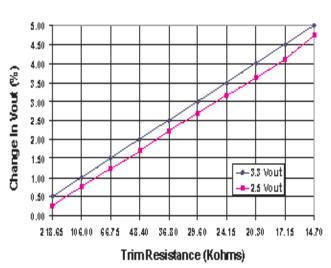
Efficiency vs. Input Voltage
@ T<sub>A</sub> = +25°C



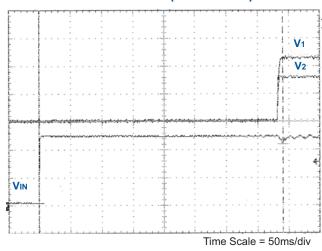
Irim Up Graph



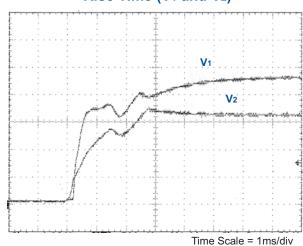
**Trim Down Graph** 



Turn On Time (VIN to VOUT)



Rise Time (V<sub>1</sub> and V<sub>2</sub>)



# PERFORMANCE CURVES: WPA60R48D3325C and WPA60R48D3325-1C

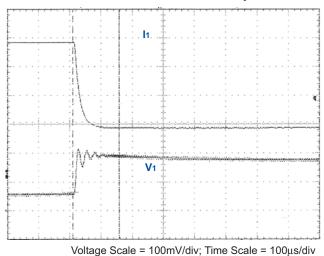
#### Turn Off Time (VIN to VOUT)

# V1 Total organism should see 100ks/e V2 VIN Ch 1 = V, Ch 2 = V2 Ch 3 = V45 Vin = V45 To = FL

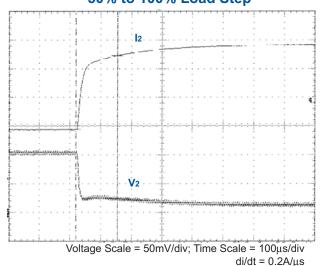
#### Time Scale = 500μs/div

 $di/dt = 0.2A/\mu s$ 

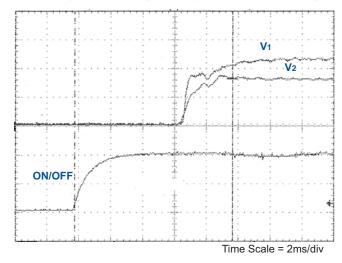
# CH1 (3.3Vdc) Transient Response 50% to 100% Load Step



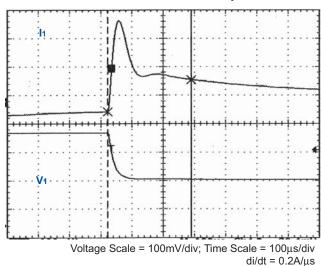
CH2 (2.5Vdc) Transient Response 50% to 100% Load Step



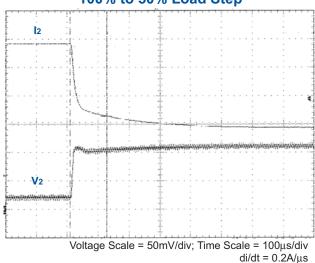
#### **Primary On Time (Primary Remote to Vout)**



CH1 (3.3Vdc) Transient Response 100% to 50% Load Step

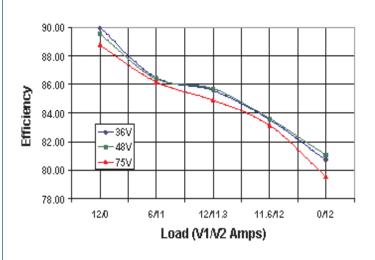


CH2 (2.5Vdc) Transient Response 100% to 50% Load Step

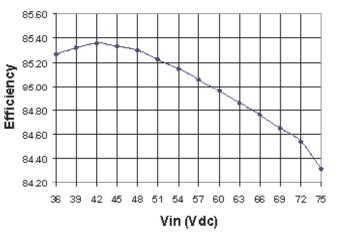


# PERFORMANCE CURVES: WPA60R48D3318C and WPA60R48D3318-1C

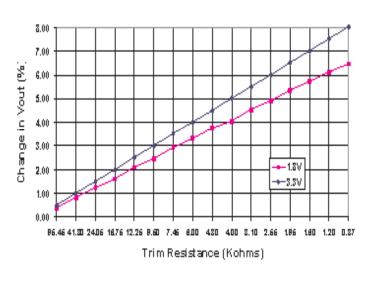
#### Efficiency vs. Output Current @ T<sub>A</sub> = +25°C



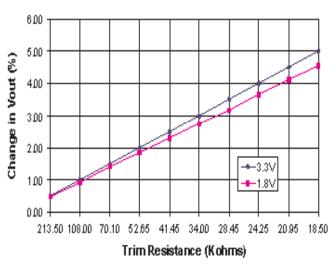
## Efficiency vs. Input Voltage @ T<sub>A</sub> = +25°C



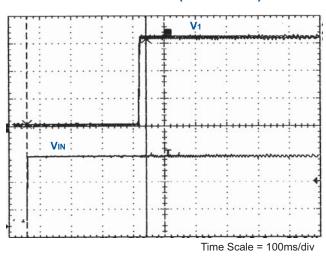
**Trim Up Graph** 



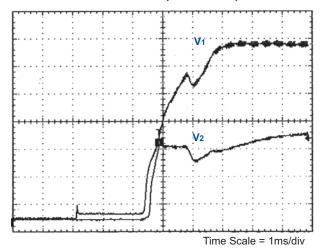
**Trim Down Graph** 



Turn On Time (VIN to VOUT)

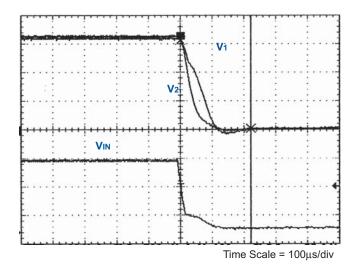


Rise Time (V<sub>1</sub> and V<sub>2</sub>)

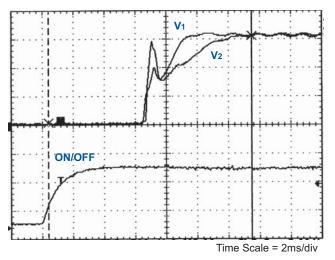


# PERFORMANCE CURVES: WPA60R48D3318C and WPA60R48D3318-1C

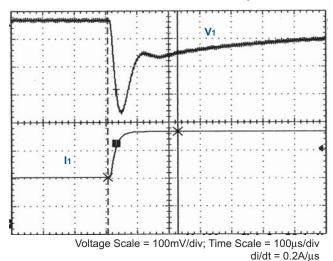
#### Turn Off Time (VIN to VOUT)



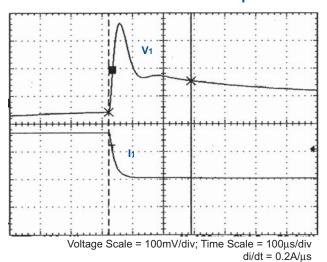
# **Primary On Time (Primary Remote to Vout)**



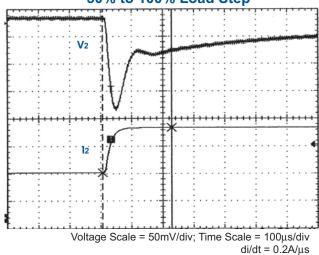
CH1 (3.3Vdc) Transient Response 50% to 100% Load Step



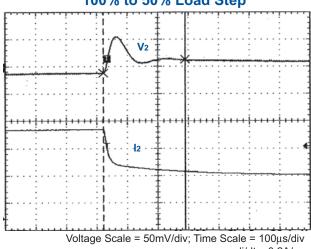
CH1 (3.3Vdc) Transient Response 100% to 50% Load Step



CH2 (1.8Vdc) Transient Response 50% to 100% Load Step



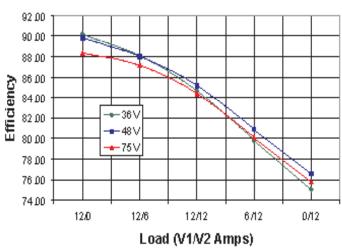
CH2 (1.8Vdc) Transient Response 100% to 50% Load Step



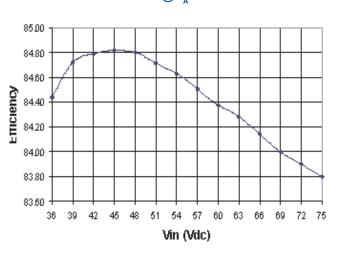
 $di/dt = 0.2A/\mu s$ 

# PERFORMANCE CURVES: WPA60R48D3312C and WPA60R48D3312-1C

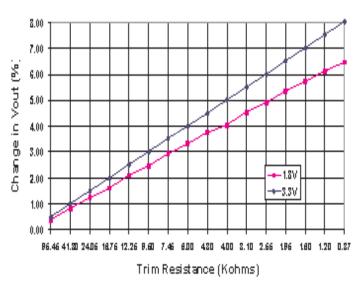
#### Efficiency vs. Output Current @ T<sub>a</sub> = +25°C



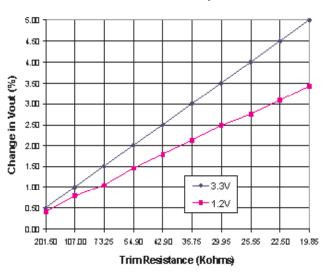
# Efficiency vs. Input Voltage @ T<sub>A</sub> = +25°C



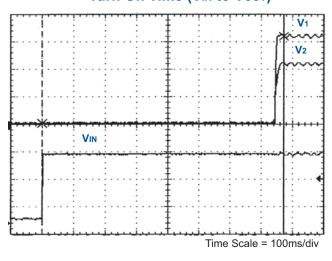
# Trim Up Graph



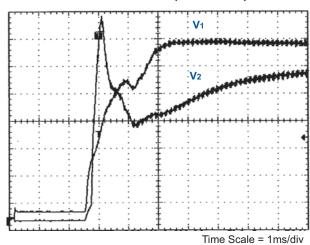
#### **Trim Down Graph**



#### TURN ON TIME (VIN TO VOUT)

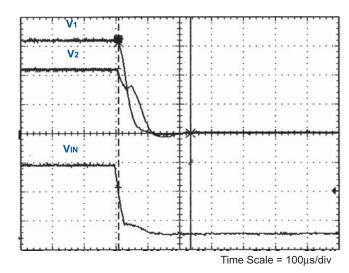


#### Rise Time (V<sub>1</sub> and V<sub>2</sub>)

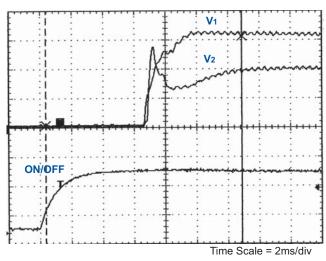


# PERFORMANCE CURVES: WPA60R48D3312C and WPA60R48D3312-1C

#### Turn Off Time (VIN to VOUT)

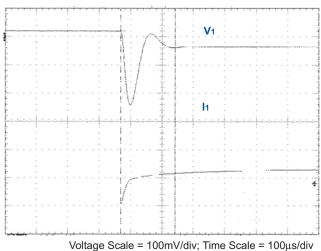


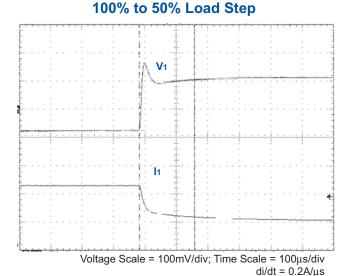
# Primary On Time (Primary Remote to Vouт)



CH1 (3.3Vdc) Transient Response

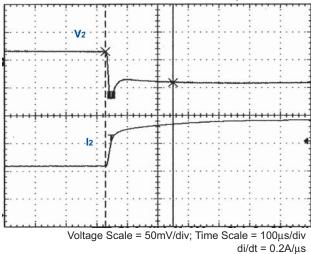
# CH1 (3.3Vdc) Transient Response 50% to 100% Load Step





voltage Scale – Toomv/div, Time Scale – Tooms/div di/dt =  $0.2A/\mu s$ 

CH2 (1.2Vdc) Transient Response 50% to 100% Load Step



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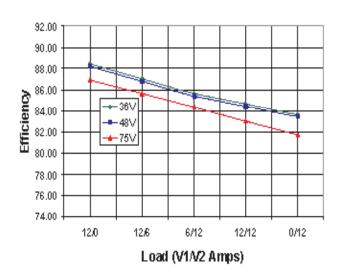
CH2 (1.2Vdc) Transient Response

100% to 50% Load Step

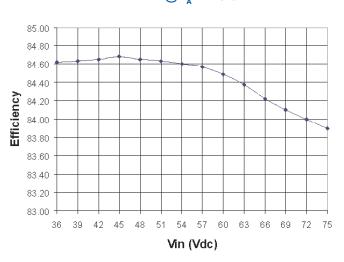
Voltage Scale = 50mV/div; Time Scale = 100μs/div di/dt = 0.2A/μs

# PERFORMANCE CURVES: WPA60R48D2518C and WPA60R48D2518-1C

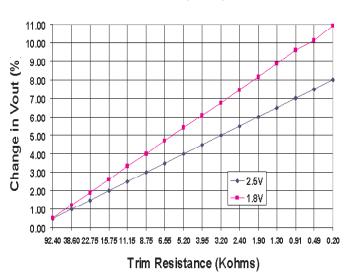
# Efficiency vs. Output Current @ T = +25°C



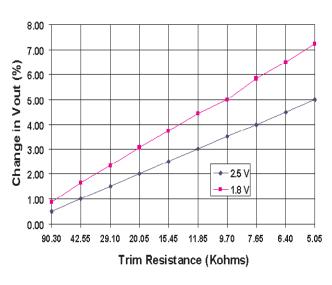
Efficiency vs. Input Voltage
@ T<sub>A</sub> = +25°C



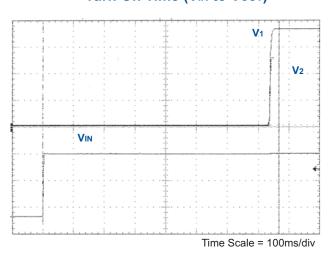
**Trim Up Graph** 



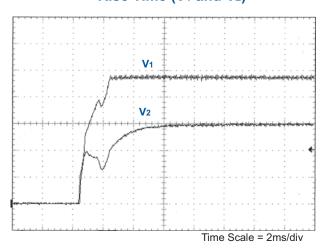
**Trim Down Graph** 



Turn On Time (VIN to VOUT)

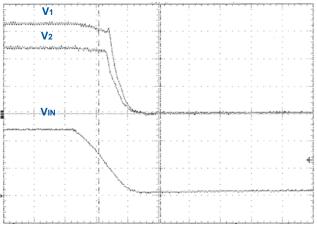


Rise Time (V<sub>1</sub> and V<sub>2</sub>)



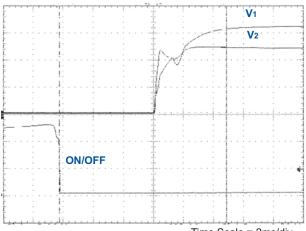
# PERFORMANCE CURVES: WPA60R48D2518C and WPA60R48D2518-1C

#### Turn Off Time (VIN to VOUT)



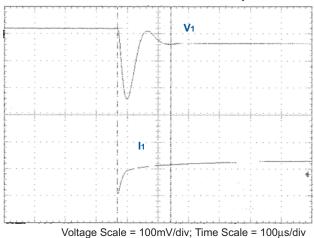
Time Scale = 500μs/div

### **Primary On Time (Primary Remote to Vout)**



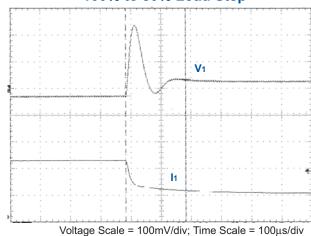
Time Scale = 2ms/div

# CH1 (2.5Vdc) Transient Response 50% to 100% Load Step



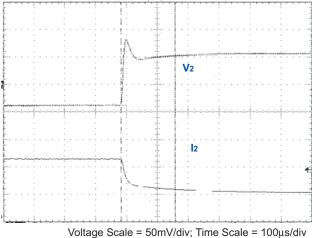
Voltage Scale = 100mV/div; Time Scale = 100µs/div di/dt = 0.2A/µs

#### CH1 (2.5Vdc) Transient Response 100% to 50% Load Step



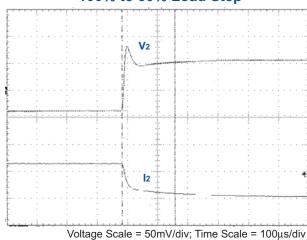
Voltage Scale = 100mV/div; Time Scale = 100μs/div di/dt = 0.2A/μs

# CH2 (1.8Vdc) Transient Response 50% to 100% Load Step



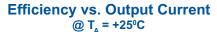
Voltage Scale = 50mV/div; Time Scale = 100μs/div di/dt = 0.2A/μs

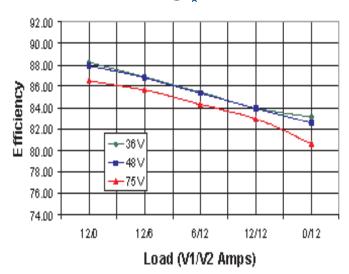
## CH2 (1.8Vdc) Transient Response 100% to 50% Load Step



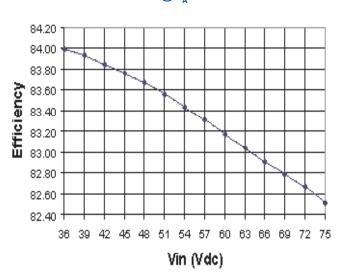
di/dt = 0.2A/µs

# PERFORMANCE CURVES: WPA60R48D2215C and WPA60R48D2215-1C





Efficiency vs. Input Voltage
@ T<sub>A</sub> = +25°C



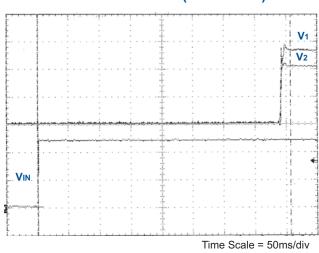
**Trim Up Graph** 



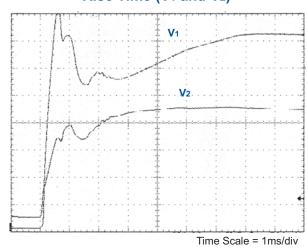
Trim Down Graph



Turn On Time (VIN to VOUT)

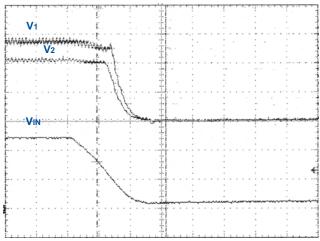


Rise Time (V<sub>1</sub> and V<sub>2</sub>)



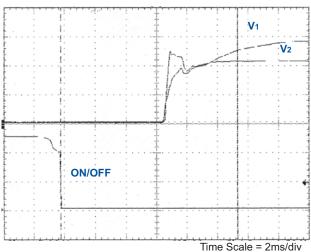
# PERFORMANCE CURVES: WPA60R48D2215C and WPA60R48D2215-1C

#### Turn Off Time (VIN to VOUT)



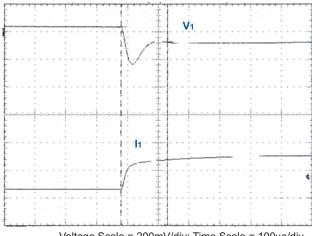
#### Time Scale = 500µs/div

#### **Primary On Time (Primary Remote to Vout)**



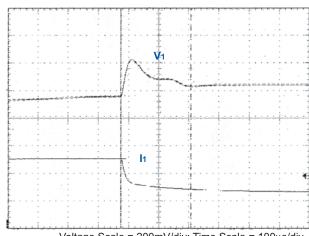
Time Scale = 2ms/di

CH1 (2.2Vdc) Transient Response 50% to 100% Load Step



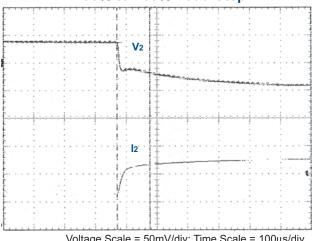
Voltage Scale = 200mV/div; Time Scale =  $100\mu s/div$  di/dt =  $0.2A/\mu s$ 

#### CH1 (2.2Vdc) Transient Response 100% to 50% Load Step



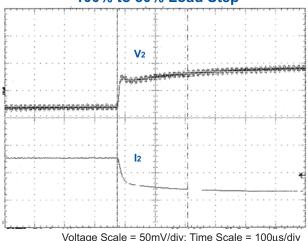
Voltage Scale = 200mV/div; Time Scale = 100µs/div di/dt = 0.2A/µs

# CH2 (1.5Vdc) Transient Response 50% to 100% Load Step



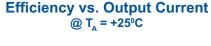
Voltage Scale = 50mV/div; Time Scale =  $100\mu s/div$ di/dt =  $0.2A/\mu s$ 

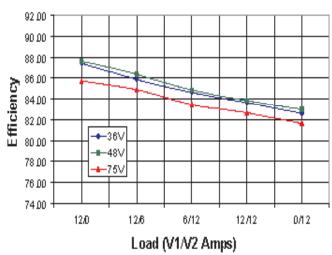
#### CH2 (1.5Vdc) Transient Response 100% to 50% Load Step



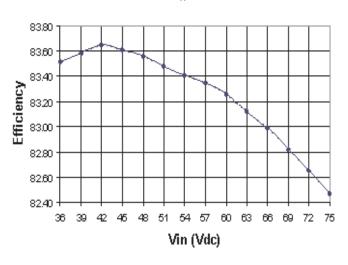
Voltage Scale = 50mV/div; Time Scale = 100μs/div di/dt = 0.2A/μs

# PERFORMANCE CURVES: WPA60R48D1812C and WPA60R48D1812-1C

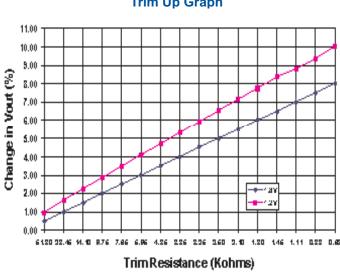




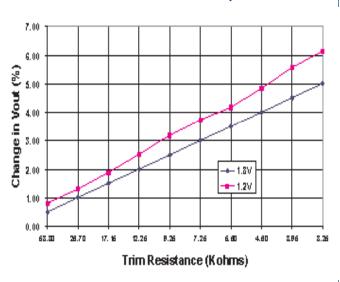
Efficiency vs. Input Voltage  $@T_A = +25^{\circ}C$ 



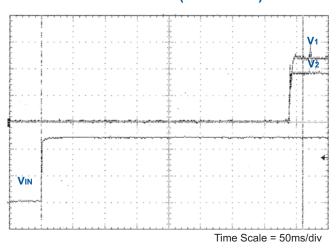
**Trim Up Graph** 



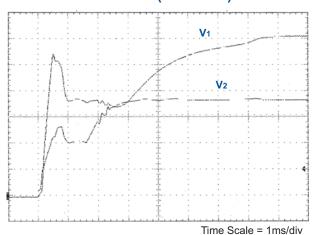
**Trim Down Graph** 



Turn On Time (VIN to VOUT)

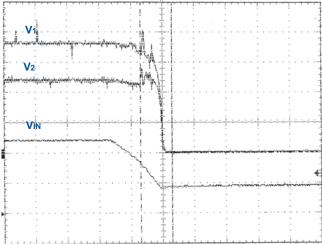


Rise Time (V<sub>1</sub> and V<sub>2</sub>)



# PERFORMANCE CURVES: WPA60R48D1812C and WPA60R48D1812-1C

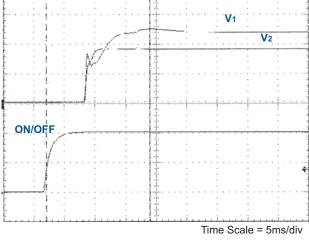
#### Turn Off Time (VIN to VOUT)



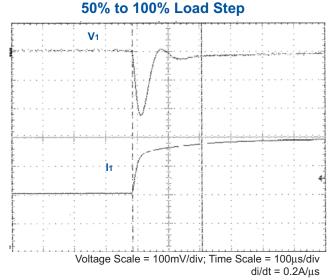
CH1 (1.8Vdc) Transient Response

#### Time Scale = 500µs/div

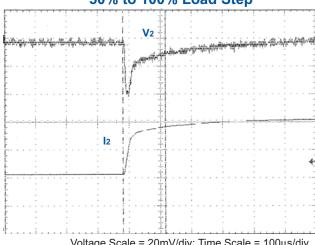
### CH1 (1.8Vdc) Transient Response 100% to 50% Load Step



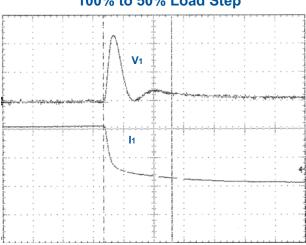
Primary On Time (Primary Remote to Vout)



## CH2 (1.2Vdc) Transient Response 50% to 100% Load Step

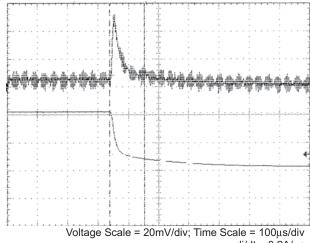


Voltage Scale = 20mV/div; Time Scale = 100μs/div  $di/dt = 0.2A/\mu s$ 



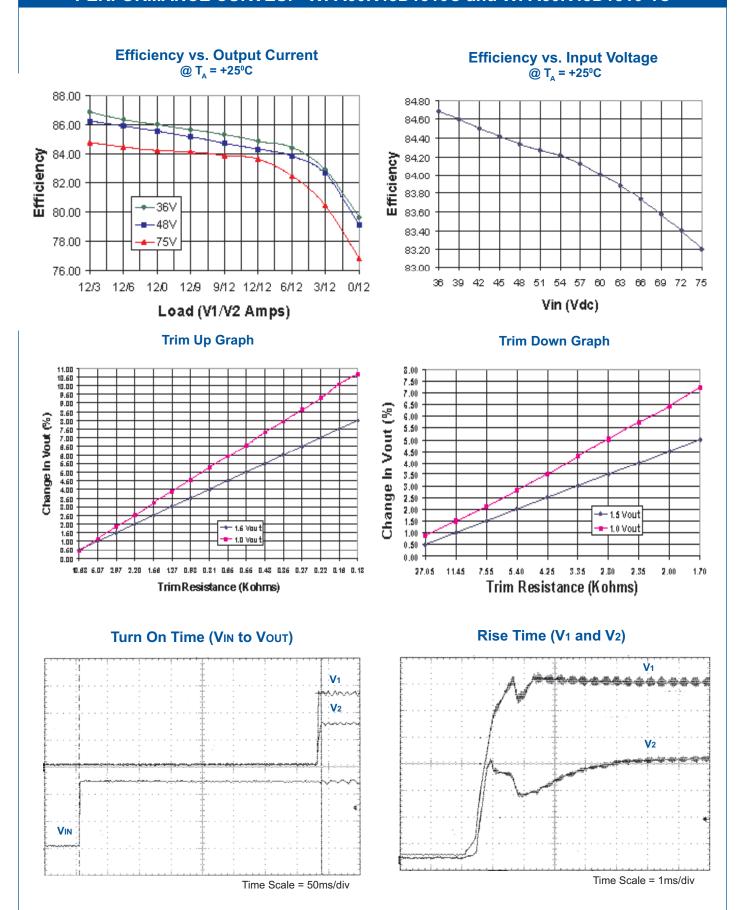
Voltage Scale = 100mV/div; Time Scale = 100μs/div  $di/dt = 0.2A/\mu s$ 

#### CH2 (1.2Vdc) Transient Response 100% to 50% Load Step



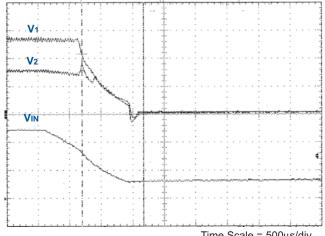
 $di/dt = 0.2A/\mu s$ 

# PERFORMANCE CURVES: WPA60R48D1510C and WPA60R48D1510-1C



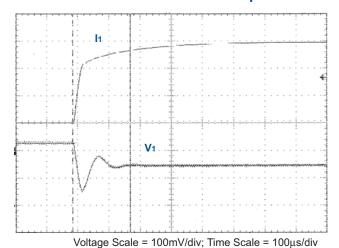
# PERFORMANCE CURVES: WPA60R48D1510C and WPA60R48D1510-1C

#### Turn Off Time (VIN to VOUT)



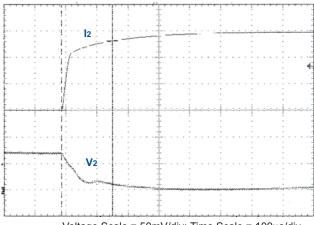
Time Scale = 500µs/div

#### CH1 (1.5Vdc) Transient Response 50% to 100% Load Step



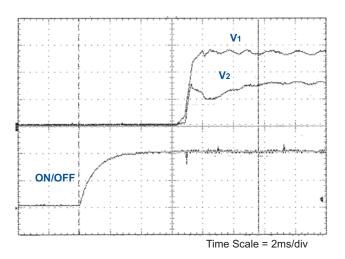
 $di/dt = 0.2A/\mu s$ 

#### CH2 (1.0Vdc) Transient Response 50% to 100% Load Step

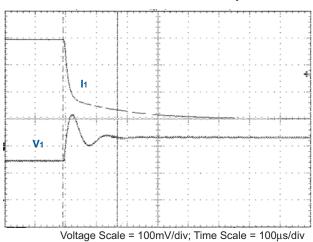


Voltage Scale = 50mV/div; Time Scale = 100μs/div  $di/dt = 0.2A/\mu s$ 

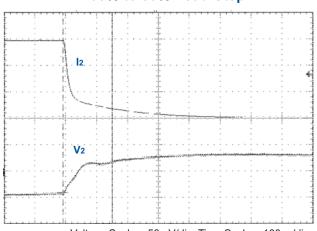
#### **Primary On Time (Primary Remote to Vout)**



CH1 (1.5Vdc) Transient Response 100% to 50% Load Step



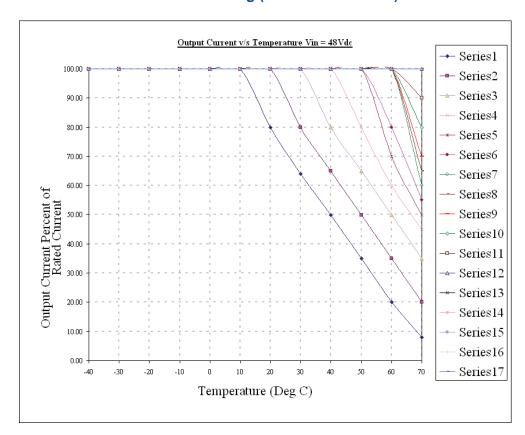
CH2 (1.0Vdc) Transient Response 100% to 50% Load Step



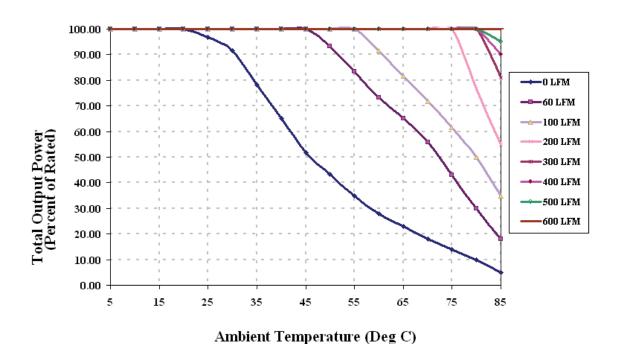
Voltage Scale = 50mV/div; Time Scale = 100μs/div  $di/dt = 0.2A/\mu s$ 

 $di/dt = 0.2A/\mu s$ 

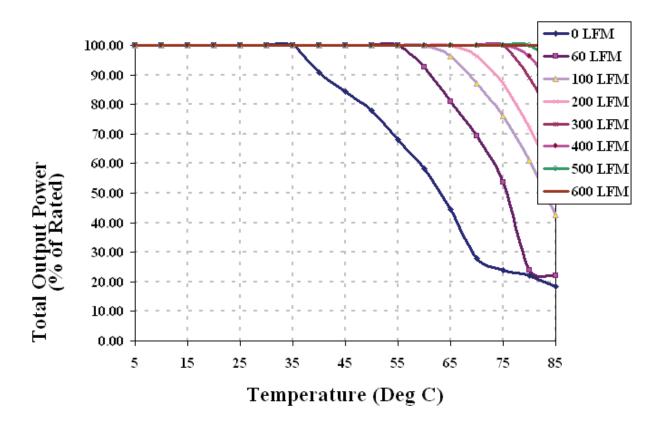
#### Thermal Derating (WPA60R48D0533C)



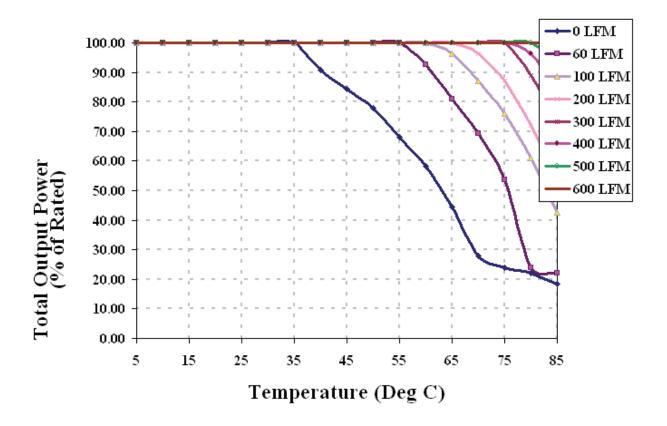
#### Thermal Derating (WPA60R48D0515C)



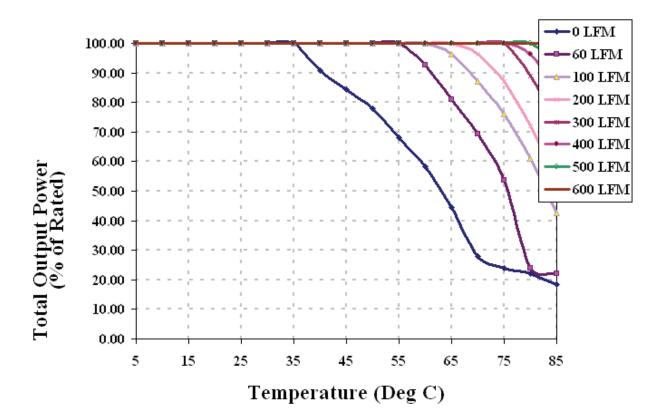
# Thermal Derating (WPA60R48D3325C)



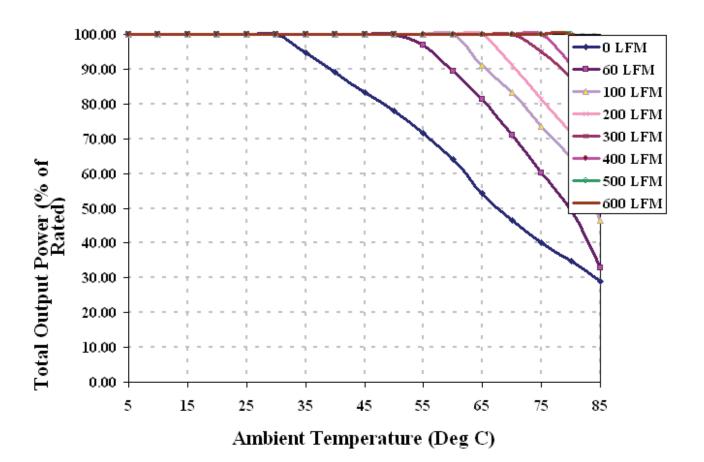
# Thermal Derating (WPA60R48D3318C)



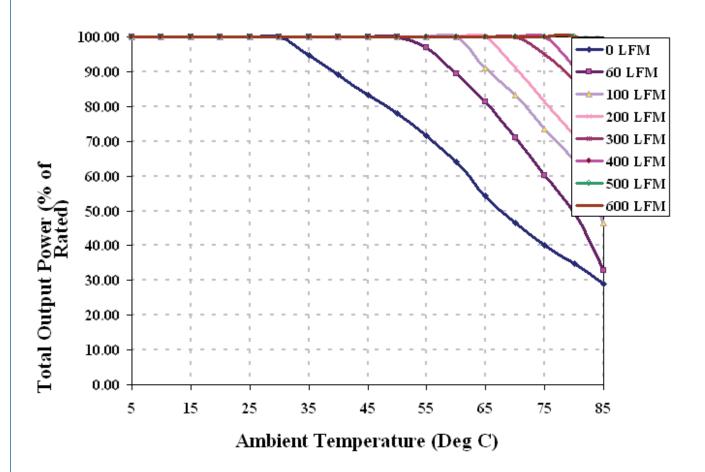
## Thermal Derating (WPA60R48D3312C)



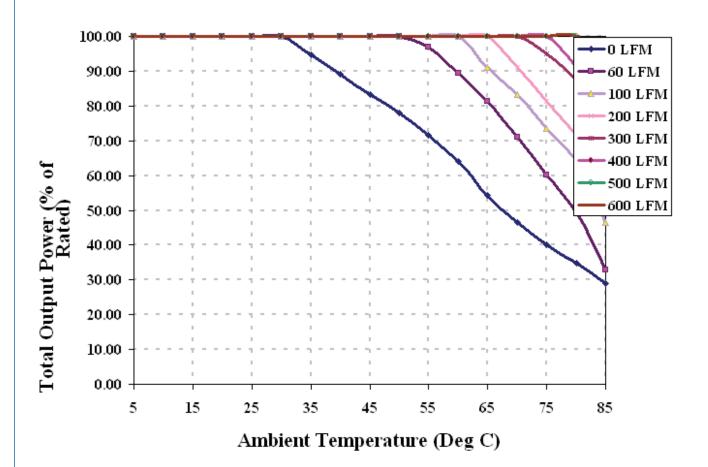
#### Thermal Derating (WPA60R48D2518C)



## Thermal Derating (WPA60R48D2215C)

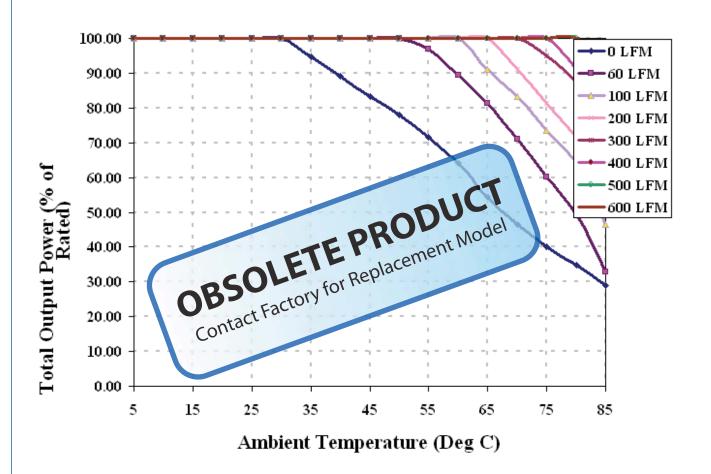


#### Thermal Derating (WPA60R48D1812C)



#### Thermal Derating (WPA60R48D1510C)

#### Output Current v/s Temperature Vin = 48Vdc



#### **Power Electronics Division, Americas**

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